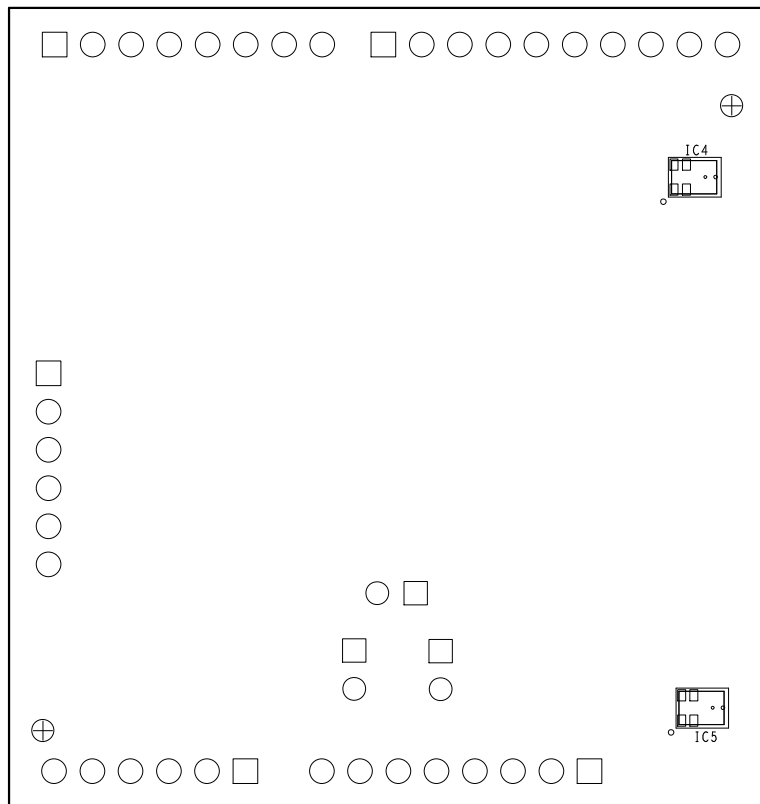
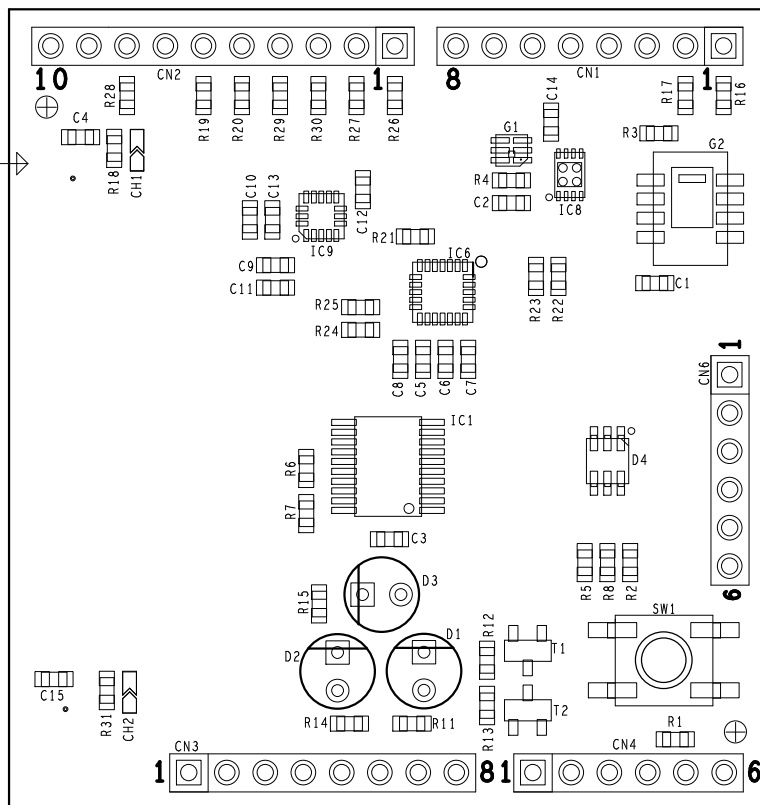

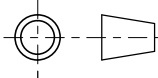


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Date	Draw by	Verify	Modifications			IND.	
Date : 29/01/19 Draw by : ASCO-SR Checked by:V.FILLATRE Project : JN5189			Material : . File.	Marging : +/-0.1	Nbr of layers :		
			Finition : .	Manufact.level : .	Silkscreen TOP :. BOTTOM:.		
			Protection:.	Scale : 2/1			
NXP Semiconductors 2,Esplanade Anton Philips Campus Effisience Colombelles BP 2000 - 14906 CAEN CEDEX 9					All rights are reserved. Reproduction in whole or in part is prohibited without the written consent of the copyright owner		
ASSEMBLY DRAWING TOP/BOTTOM OM15081-2				SIZE	PCB2460-2-400	REV.	PL.
JN5189 AUDIO/LIGHT/SENSORS EXPANSION BOARD				A4		00	1/1

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