

## Mask Set Errata for Mask 2N10J

This report applies to mask 2N10J for these products:

- S08PL16
- S08PL8

**Table 1. Errata and Information Summary**

Erratum ID	Erratum Title
ERR006657	ADC: ADC FIFO not working when the bus clock is slower than ADC clock divided by 2
ERR005264	DBG: Comparator C with TAG type can not generate breakpoint when setting breakpoint at the address other than instruction opcode address

**Table 2. Revision History**

Revision	Changes
20201029	Initial revision

### **ERR006657: ADC: ADC FIFO not working when the bus clock is slower than ADC clock divided by 2**

**Description:** When the ADC FIFO mode is enabled, the FIFO can not get correct result if the bus clock is slower than ADC conversion clock divided by 2.

**Workaround:** Configure the bus clock to be faster than the ADC conversion clock (ADCK) divided by 2 if the ADC FIFO is used.



**ERR005264: DBG: Comparator C with TAG type can not generate breakpoint when setting breakpoint at the address other than instruction opcode address**

**Description:** When setting breakpoint at the address other than instruction opcode address, the comparator C with TAG type can not generate breakpoint. This issue does not affect code execution.

**Workaround:** If such tag breakpoint at the address other than instruction opcode address is required, use comparator A and/or B tag breakpoint.

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